

	Type	Hits	Search Text	DBs	Time Stamp
1	IS&R	1	("6539025").PN.	USPAT; US-PGPUB; EPO; JPO	2004/06/23 15:46
2	IS&R	1	("6430153").PN.	USPAT; US-PGPUB; EPO; JPO	2004/06/22 15:38
3	IS&R	1	("6345040").PN.	USPAT; US-PGPUB; EPO; JPO	2004/06/22 15:39
4	IS&R	1	("6262986").PN.	USPAT; US-PGPUB; EPO; JPO	2004/06/22 15:40
5	IS&R	1	("6141346").PN.	USPAT; US-PGPUB; EPO; JPO	2004/06/22 15:41
6	BRS	289	370/394.ccls. and @ad<19990823	USPAT; US-PGPUB; EPO; JPO	2004/06/22 17:08
7	BRS	1	(370/394.ccls. and @ad<19990823) and pointer and (logic near queue)	USPAT; US-PGPUB; EPO; JPO	2004/06/22 16:05
8	BRS	4	(370/394.ccls. and @ad<19990823) and pointer and (logic with queue)	USPAT; US-PGPUB; EPO; JPO	2004/06/22 16:05
9	BRS	5	(370/394.ccls. and @ad<19990823) and ((head near4 line) hol)	USPAT; US-PGPUB; EPO; JPO	2004/06/22 17:13
10	BRS	1	((370/394.ccls. and @ad<19990823) and ((head near4 line) hol)) and pointer	USPAT; US-PGPUB; EPO; JPO	2004/06/22 17:13
11	IS&R	1	("6128295").PN.	USPAT; US-PGPUB; EPO; JPO	2004/06/23 15:46
12	BRS	1332	bit near slice and @ad<19990823	USPAT; US-PGPUB; EPO; JPO	2004/06/24 09:52
13	BRS	1	(bit near slice and @ad<19990823) and (redundant near switch)	USPAT; US-PGPUB; EPO; JPO	2004/06/24 09:54
14	BRS	5	(bit near slice and @ad<19990823) and (slice near switch)	USPAT; US-PGPUB; EPO; JPO	2004/06/24 09:55

	Type	Hits	Search Text	DBs	Time Stamp
1	BRS	30	((370/429   370/428).CCLS.) and (((((packet near switch) and (input near (buffer memory RAM SRAM DRAM))) AND (370/\$).CCLS.) and packet) and queue)	USPAT; EPO; JPO	2003/12/13 09:36
2	BRS	1464	370/428,429.ccls.	USPAT; EPO; JPO	2003/12/13 10:10
3	BRS	2687	packet adj1 switch	USPAT; EPO; JPO	2003/12/13 10:10
4	BRS	86	370/428,429.ccls. and (packet adj1 switch)	USPAT; EPO; JPO	2003/12/13 10:10
5	BRS	22	(370/428,429.ccls. and (packet adj1 switch)) and (queue near input)	USPAT; EPO; JPO	2003/12/13 10:12
6	BRS	147	packet and (input near (buffer memory queue storage)) and (address\$3 near manag\$5) and @ad<20000822	USPAT; US-PGPUB; EPO; JPO	2004/06/15 17:13
7	BRS	2	((packet and (input near (buffer memory queue storage)) and (address\$3 near manag\$5) and @ad<20000822) and stor\$3 and forward\$3) and (hol (head near line))	USPAT; US-PGPUB; EPO; JPO	2004/06/15 16:23
8	BRS	6	((packet and (input near (buffer memory queue storage)) and (address\$3 near manag\$5) and @ad<20000822) and stor\$3 and forward\$3) and (hol (head near3 line))	USPAT; US-PGPUB; EPO; JPO	2004/06/15 16:23
9	BRS	106	(packet and (input near (buffer memory queue storage)) and (address\$3 near manag\$5) and @ad<20000822) and stor\$3 and forward\$3	USPAT; US-PGPUB; EPO; JPO	2004/06/15 16:54
10	BRS	51	(packet and (input near (buffer memory queue storage)) and (address\$3 near manag\$5) and @ad<20000822) and stor\$3 and forward\$3 and schedul\$3	USPAT; US-PGPUB; EPO; JPO	2004/06/15 16:54
11	BRS	102	packet and (input near (buffer memory queue storage)) and (address\$3 near manag\$5) and @ad<20000822 and (output near (buffer memory queue storage))	USPAT; US-PGPUB; EPO; JPO	2004/06/15 17:14
12	BRS	28	(packet and (input near (buffer memory queue storage)) and (address\$3 near manag\$5) and @ad<20000822 and (output near (buffer memory queue storage))) and (packet near switch\$3)	USPAT; US-PGPUB; EPO; JPO	2004/06/15 17:15

	<b>U</b>	<b>1</b>	<b>Document ID</b>	<b>Issue Date</b>	<b>Pages</b>	<b>Title</b>	<b>Current OR</b>
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6539025 B1	20030325	18	Priority arbitration for point-to-point and multipoint transmission	370/414
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6430153 B1	20020806	15	Trunk delay simulator	370/230.1
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6345040 B1	20020205	9	Scalable scheduled cell switch and method for switching	370/232
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6262986 B1	20010717	33	Method and apparatus for packet scheduling using queue length and connection weight	370/399
5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6141346 A	20001031	13	Point-to-multipoint transmission using subqueues	370/390
6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6181678 B1	20010130	33	Binary-tree data element sorting device and ATM spacer comprising such a device	370/235
7	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 4949248 A	19900814	19	System for shared remote access of multiple application programs executing in one or more computers	709/203
8	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20010010692 A1	20010802	41	MEMORY ORGANIZATION IN A SWITCHING DEVICE	370/395.7
9	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6556571 B1	20030429	13	Fast round robin priority port scheduler for high capacity ATM switches	370/395.4
10	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5406556 A	19950411	25	Output buffered packet switch with a flexible buffer management scheme	370/381
11	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5440549 A	19950808	17	Broadband multi-channel switch with multicasting capability	370/394
12	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6128295 A	20001003	28	Buffering of point-to-point and/or point-to-multipoint ATM cells	370/389
13	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6611527 B1	20030826	20	Packet switching apparatus with a common buffer	370/412
14	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20030179712 A1	20030925	1116	CONNECTIONLESS COMMUNICATIONS SYSTEM, ITS TEST METHOD, AND INTRA-STATION CONTROL SYSTEM	370/249
15	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6249520 B1	20010619	74	High-performance non-blocking switch with multiple channel ordering constraints	370/368
16	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6026075 A	20000215	14	Flow control mechanism	370/236

	U	1	Document ID	Issue Date	Pages	Title	Current OR
17	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5936958 A	19990810	44	ATM exchange for monitoring congestion and allocating and transmitting bandwidth-guaranteed and non-bandwidth-guaranteed connection calls	370/395.43
18	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5463622 A	19951031	18	Control unit for the common memory of an ATM node	370/399
19	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5237565 A	19930817	29	Multiple path, self-routing switching network for switching asynchronous time division multiplex cells	370/236
20	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5517495 A	19960514	20	Fair prioritized scheduling in an input-buffered switch	370/399
21	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 4964119 A	19901016	33	Method and system for packet exchange	370/237